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Complete If Known

Application Number	10/675,872
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Vo, Ted T.
Art Unit	2192
Attorney Docket No.	AUS920030487US1

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

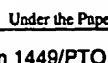
NON-PATENT LITERATURE

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TTU	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
TTU	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, lp.com, IPCOM000012728D, pp. 1-2
TTU	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner: TED T. VO

Date Considered: 5/19/06

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NON PATENT LITERATURE

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TH	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
TH	AI	TORRELLAS et al., "False Sharing and Spatial Locality In Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
TH	AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

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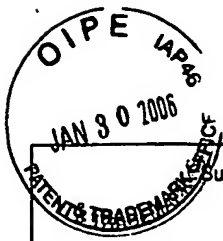
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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TTV	AA 5,754,839	May 19, 1998	Pardo et al.	
TTV	AB 6,378,064 B1	Apr. 23, 2002	Edwards et al.	
TTV	AC 2003/0154463 A1	Aug. 14, 2003	Betker et al.	
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TTV	AE 5,404,500	Apr. 4, 1995	Legvold et al.	
TTV	AF 6,253,338 B1	Jun. 26, 2001	Smolders	
TTV	AG 6,145,123	Nov. 7, 2000	Torrey et al.	
TTV	AH 2005/0102493 A1	May 12, 2005	DeWitt, Jr. et al.	
TTV	AI 6,240,510 B1	May 29, 2001	Yeh et al.	
TTV	AJ 6,560,693 B1	May 6, 2003	Puzak et al.	
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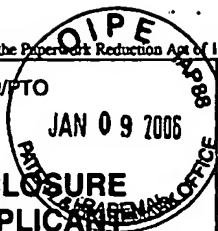
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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TTU	AA 4,034,353	07-1977	Denny et al.	
TTU	AB 4,821,178	04-1989	Levin et al.	
TTU	AC 5,151,981	09-1992	Westcott et al.	
TTU	AD 5,581,482	12-1996	Wiedenman et al.	
TTU	AE 5,594,864	01-1997	Trauben	
TTU	AF 5,691,920	11-1997	Levine et al.	
TTU	AG 5,758,168	05-1998	Mealey et al.	
TTU	AH 6,480,966	11-2002	Rawson, III	
TTU	AI 2003/0135720	07-2003	DeWitt et al.	
TTU	AJ 5,926,640	07-1999	Mason et al.	
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TTU	AW Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 nd Ed., 1982, pp.434-443.
TTU	AX Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner: **TED T. VO**

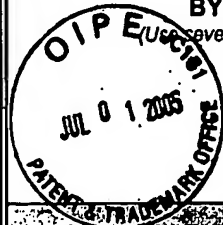
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LIST OF PRIOR ART CITED
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ATTORNEY DOCKET NO.
AUS920030487US1SERIAL NO.
10/675,872

APPLICANT Levine et al.

FILING DATE September 30, 2003

GROUP ART UNIT 2192

RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
TTV	AA	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
TTV	AB	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
TTV	AC	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
TTV	AD	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
TTV	AE	10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
TTV	AF	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
TTV	AG	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
TTV	AH	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Exception on Specified Instructions	Sep. 30, 2003
TTV	AI	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003

DATE CONSIDERED 5/19/06

EXAMINER TTV

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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030487US1		SERIAL NO. N t Assigned 10/675872	
			APPLICANT DeWitt, Jr. et al.			
			FILING DATE		GROUP ART UNIT Not Assigned 2191	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
TTU	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989	
TTU	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
TTU	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000	
TTU	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
TTU	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998	
TTU	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
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TTU	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998	
TTU	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997	
TTU	AJ 5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995	
TTU	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997	
TTU	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998	
TTU	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995	
TTU	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998	
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TTU	AU 4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979	
TTU	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997	
TTU	AW 6,286,132 B1	Sep. 4, 2001	Tanaka et al.	717/4	Jan. 7, 1999	
TTU	AX 6,206,584 B1	Mar. 27, 2001	Hastings	395/183.11	May 31, 1995	
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TTU	AZ 6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997	
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DATE CONSIDERED 5/19/06			EXAMINER TED T. V. D.			
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Form PTO-1449			ATTORNEY DOCKET NO. AUS920030487US1		SERIAL NO. Not Assigned	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT DeWitt, Jr. et al.			
			FILING DATE		GROUP ART UNIT Not Assigned	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME		CLASS/ SUBCLASS	FILING DATE
TTU	BB	2002/0129309	Floyd et al.		714/724	Dec. 18, 2000
TTU	BC	2001/0032305	Barry		712/34	Feb. 23, 2001
TTU	BD	2002/0199179	Lavery et al.		717/158	Jun. 21, 2001
TTU	BE	2002/0124237	Sprunt et al.		717/127	Dec. 29, 2000
TTU	BF	2002/0147965	Swaine et al.		717/124	Feb. 1, 2001
TTU	BG	2002/0019976	Patel et al.		717/137	May 25, 2001
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY		CLASS/ SUBCLASS	TRANSLATION YES NO
TTU	BH	JP2000029731	Japan (abstract only)		G06F 9/38	X
TTU	BI	JP2000347863	Japan (abstract only)		G06F 9/38	X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)						
	BJ	Kikuchi, "Parallelization Assist System", Joho Chori, Vol. 34, No. 9, Sept. 1999, pp. 1159-1169.				
TTU	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.				
TTU	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.				
	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processing Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.				
TTU	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 th , Phoenix, Feb. 20-22, 2000, pp. 516-524.				
TTU	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.				
RELATED PATENT APPLICATIONS						
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE			FILING DATE
TTU	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug			Nov. 4, 1999
TTU	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System			Oct. 2, 1995
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